



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Taketo WATANABE et al.**

Group Art Unit: **2814**

Serial No.: **10/633,538**

Examiner: **Hoai V. Pham**

Filed: **August 5, 2003**

Confirmation No.: **6451**

For: **SEMICONDUCTOR DEVICE, MANUFACTURING METHOD  
THEREOF, AND CMOS INTEGRATED CIRCUIT DEVICE**

Attorney Docket Number: **030927**

Customer Number: **38834**

**AMENDMENT**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Date: July 7, 2005

Sir:

This paper is filed in response to the Office Action dated March 10, 2005.

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks** begin on page 12 of this paper.